

IN THE SPECIFICATION:

Please replace paragraph number [0017] with the following rewritten paragraph:

[0017] The “inking out” is usually accomplished by marking over an exposed feature in the solder resist which is a component of the defective die site, completing the mark with an ink pen, for example. As used herein, the term “exposed feature” denotes an opening in the solder resist which typically exposes a visibly discernible Au/Ni/Cu surface. Features which are commonly “inked out” include pin one indicators, bond pads, and/or fiduciary marks, as shown, for example, in drawing FIG. 1, illustrating inked-out marks 34 over pin one indicators 22, where the semiconductor die sites 20 have been found defective. Accordingly, during the die attach process, defective semiconductor die sites 20 do not receive dice attached thereto and the good die semiconductor die sites 20 do receive dice 52 attached thereto.

Please replace paragraph number [0045] with the following rewritten paragraph:

[0045] Depicted in drawing FIG. 5 is the mounting substrate 200 in the block diagram of drawing FIG. 4. The mounting substrate 200 includes a substrate body 310 with a layer of solder resist formed on a surface thereof and a plurality of semiconductor die sites 320 in an array or matrix. Each semiconductor die site 320 includes a die receiving area 330 with electrical conductors interconnected thereto for semiconductor dice ~~342,344~~, 342, 344. The substrate body 310 may, for example, be a multilayer printed wiring board or carrier substrate. Formed as components of each semiconductor die site 320 are various electrical conductors, including bond pads (not shown), circuit traces (not shown), and solder ball contact pads 332, and a die receiving area 330. Other contact pads are provided on the substrate body 310 for interconnection to a mounted integrated circuit die by wire bonding or other means. Conductive (circuit) traces (not shown) underlie the layer of solder resist and extend from the other contact pads to solder ball contact pads 332, upon which solder balls (not shown) may be subsequently formed. As shown, solder ball contact pads 332 are configured in a preselected array pattern surrounding each of semiconductor die sites 320. After die attach, solder ball contact pads 332 with solder balls

disposed thereon will comprise connective elements forming a ball grid array around each semiconductor die site 320 for external communication with a printed wiring board, carrier substrate, other BGA package, module board, etc. The solder resist layer 331, which is typically an insulation film formed of an insulation resin, is disposed on substrate body 310 to prevent electrical shorting of conductive traces underlying the resist, as well as to provide protection from contaminants and aid vision systems of automated die attach apparatus in determining the correct mounting orientation for the placement of semiconductor dice on semiconductor die sites 320. Pin one indicators 322 and/or fiducial marks 324 may also be provided. On an otherwise unused peripheral surface area portion of substrate body 310, a designator 340 for encoding various identification information (discussed further below) relating to mounting substrate 200 is illustrated.